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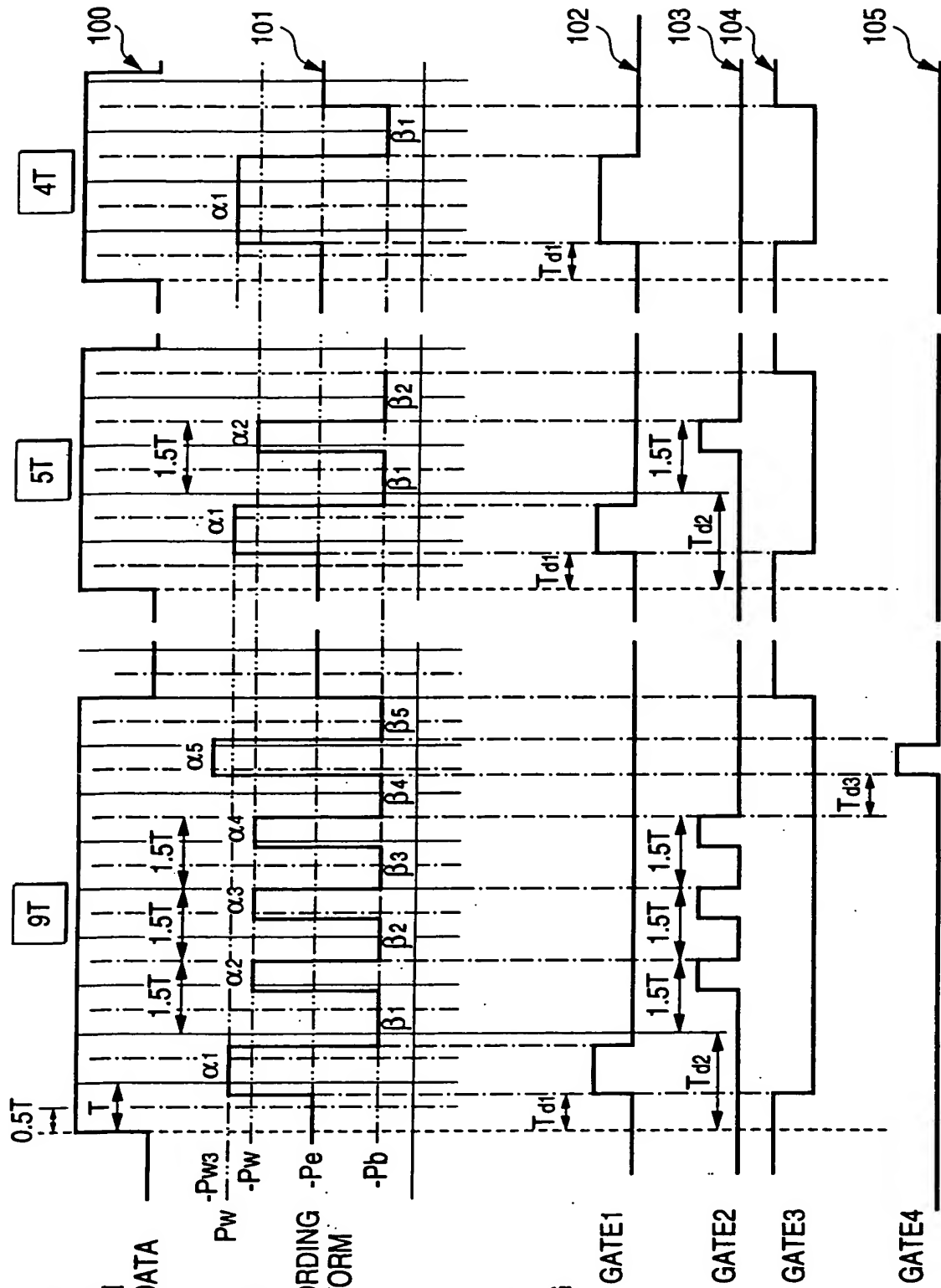


FIG. 1 (a)

MARK-LENGTH
MODULATED DATA

FIG. 1 (b)

DIVIDED RECORDING
PULSE WAVEFORM

FIG. 1 (c)

CLOCK TIMING

The diagram shows two power signals over time. The top signal, labeled P_w (RECORDING POWER), is a solid line that rises to a high level for a duration $1T$, then falls to a low level for a duration $0.5T$, and then rises again. The bottom signal, labeled P_b (BIAS POWER), is a dashed line that remains at a low level until the first $0.5T$ interval, then rises to a high level for the duration of the recording pulse, and then falls to a low level for the subsequent $0.5T$ interval. This pattern repeats, with the recording power pulse occurring during the high bias power interval.

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FIG. 3 (a)

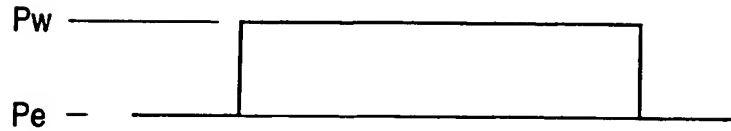


FIG. 3 (b)

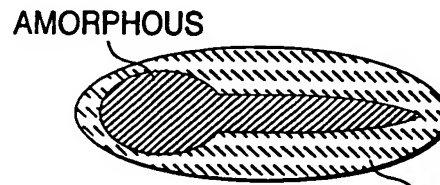


FIG. 3 (c)

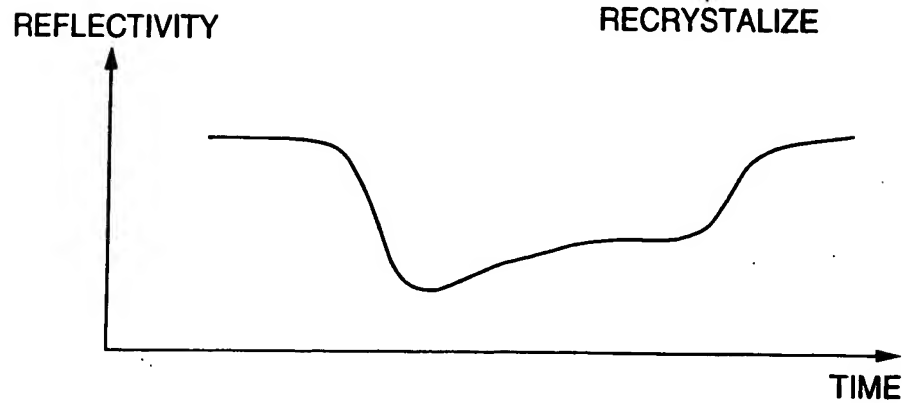


FIG. 3 (d)

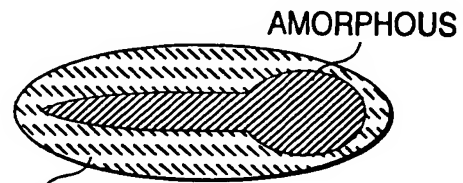
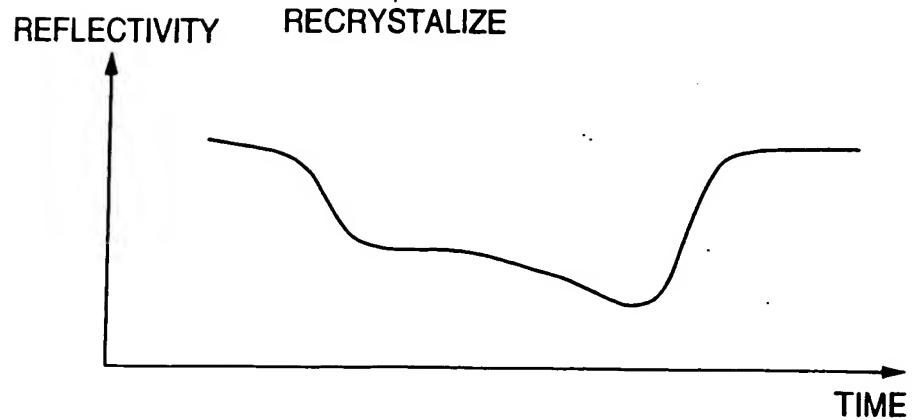


FIG. 3 (e)



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FIG. 4 (b)

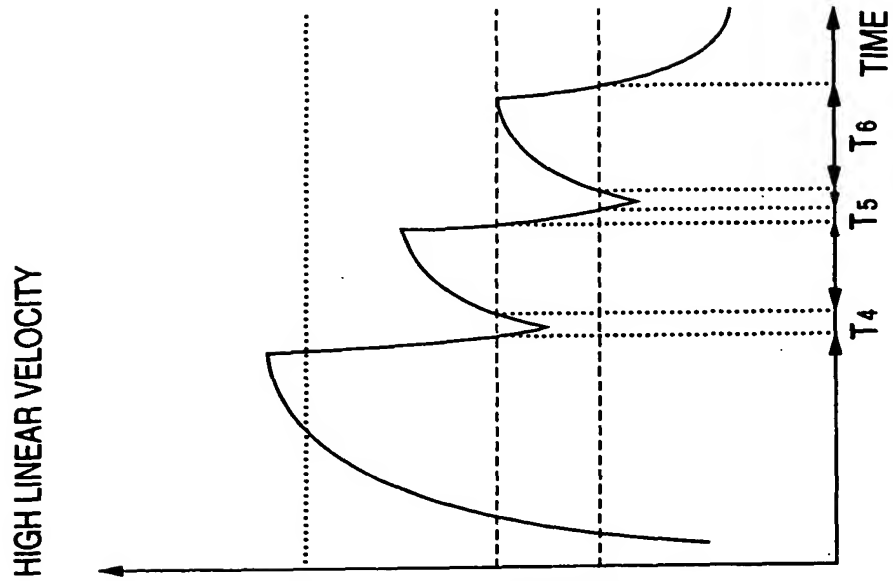
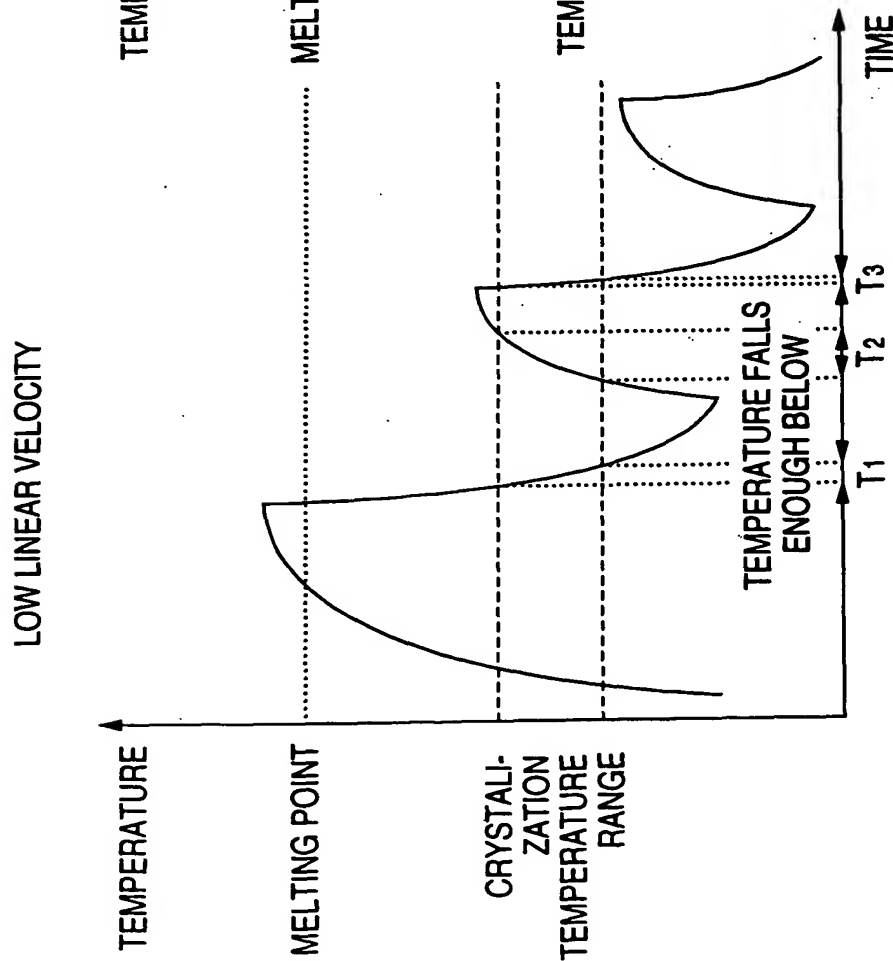
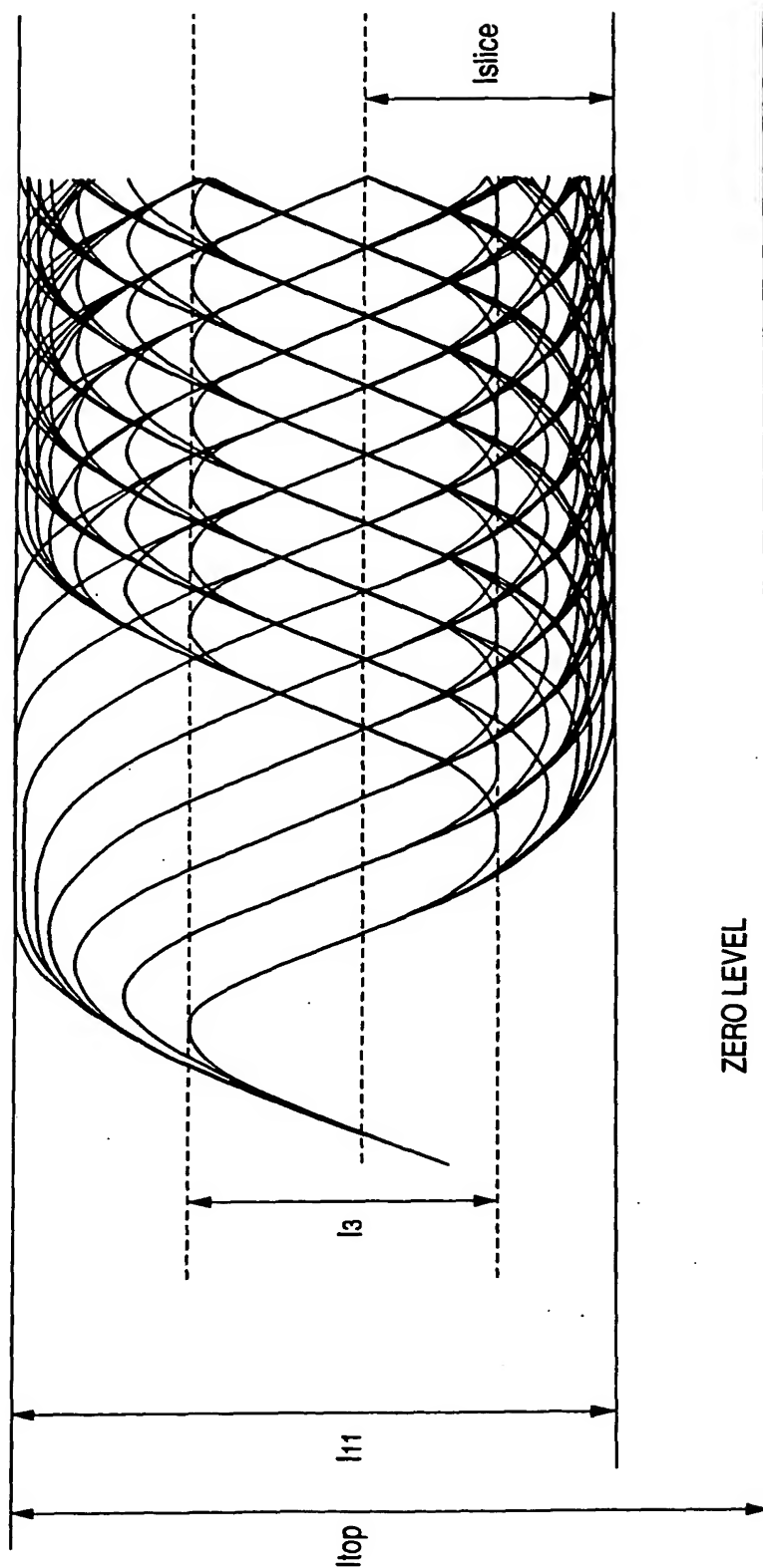


FIG. 4 (a)



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FIG. 5



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FIG. 6 (a)

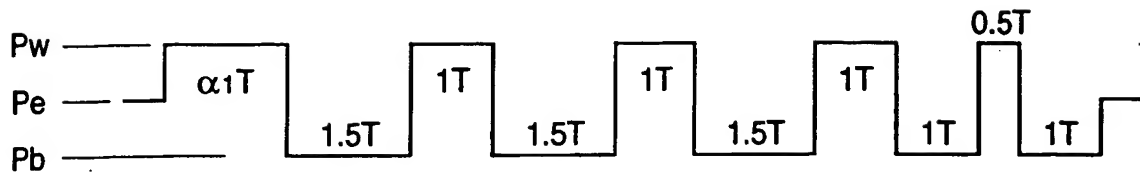


FIG. 6 (b)

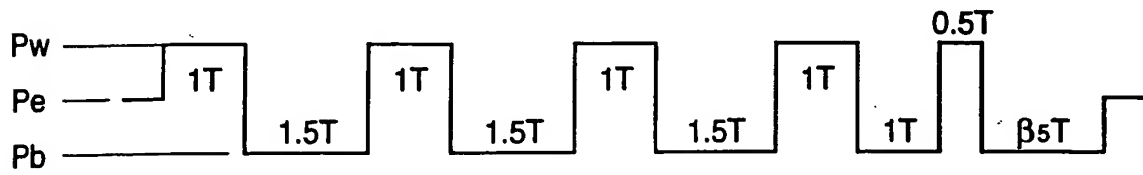
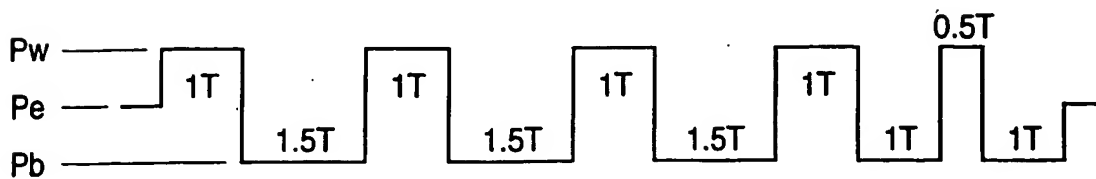


FIG. 6 (c)



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FIG. 7

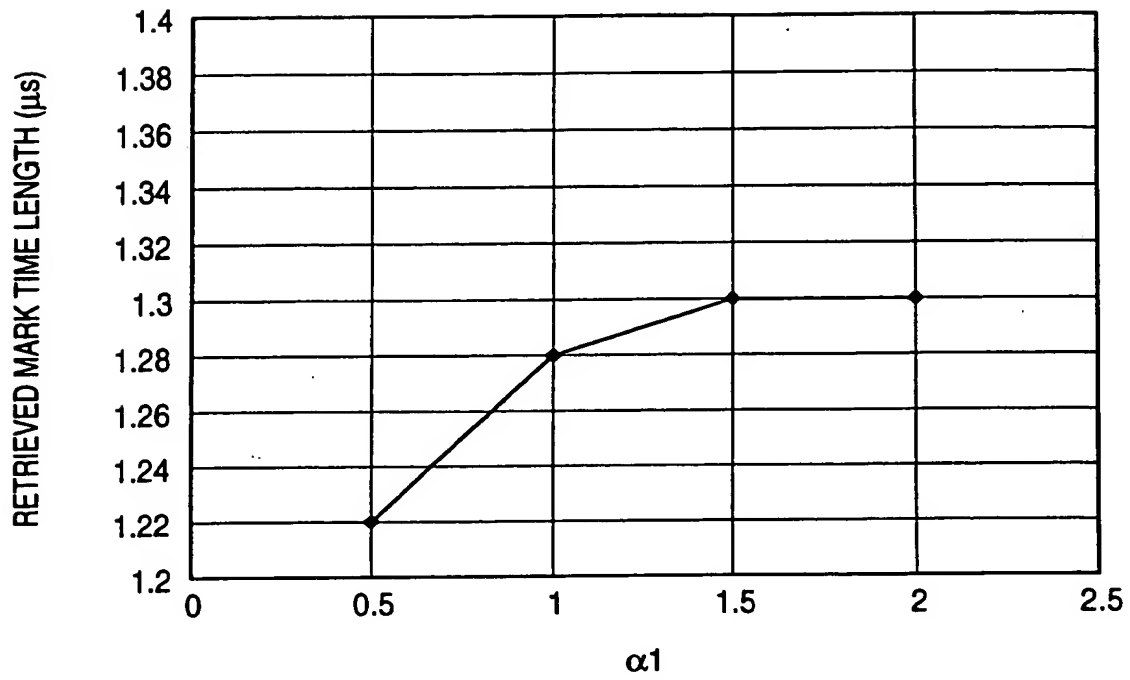
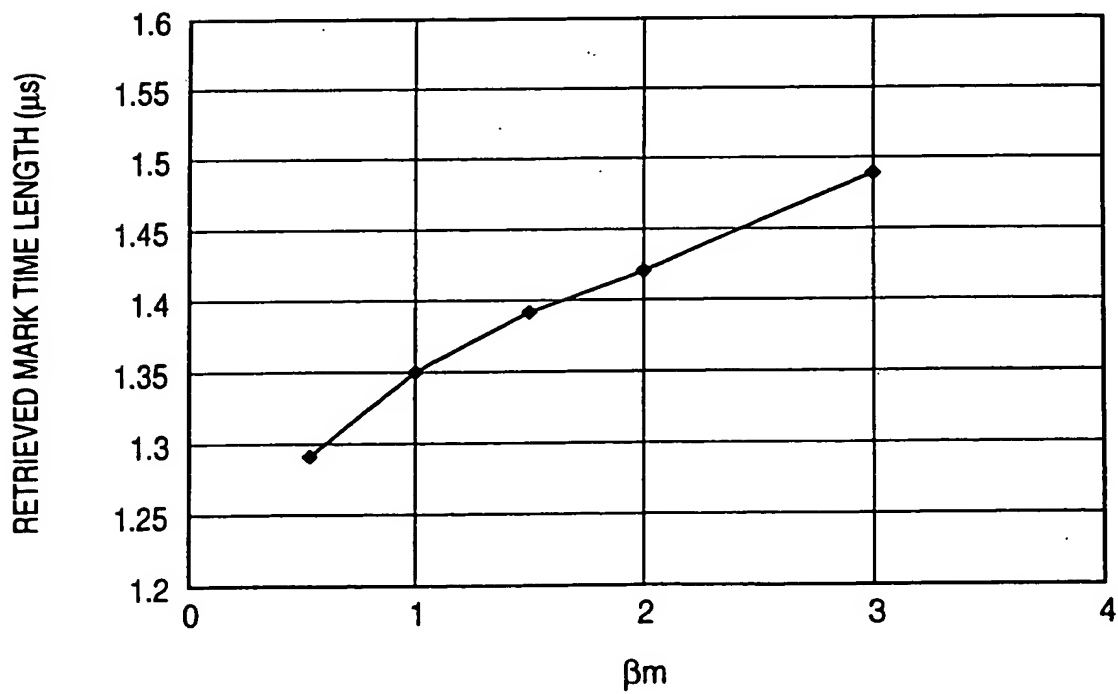
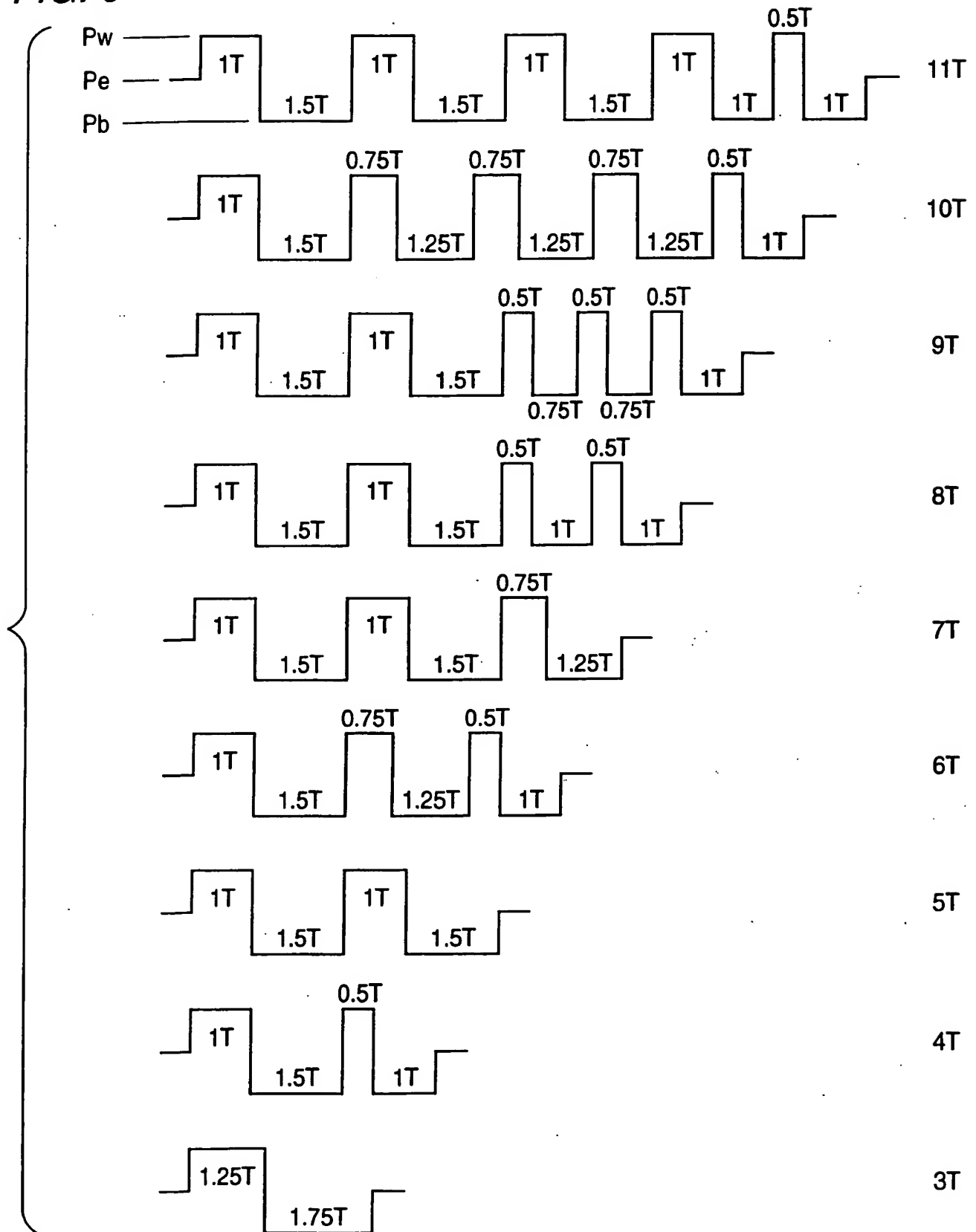


FIG. 8



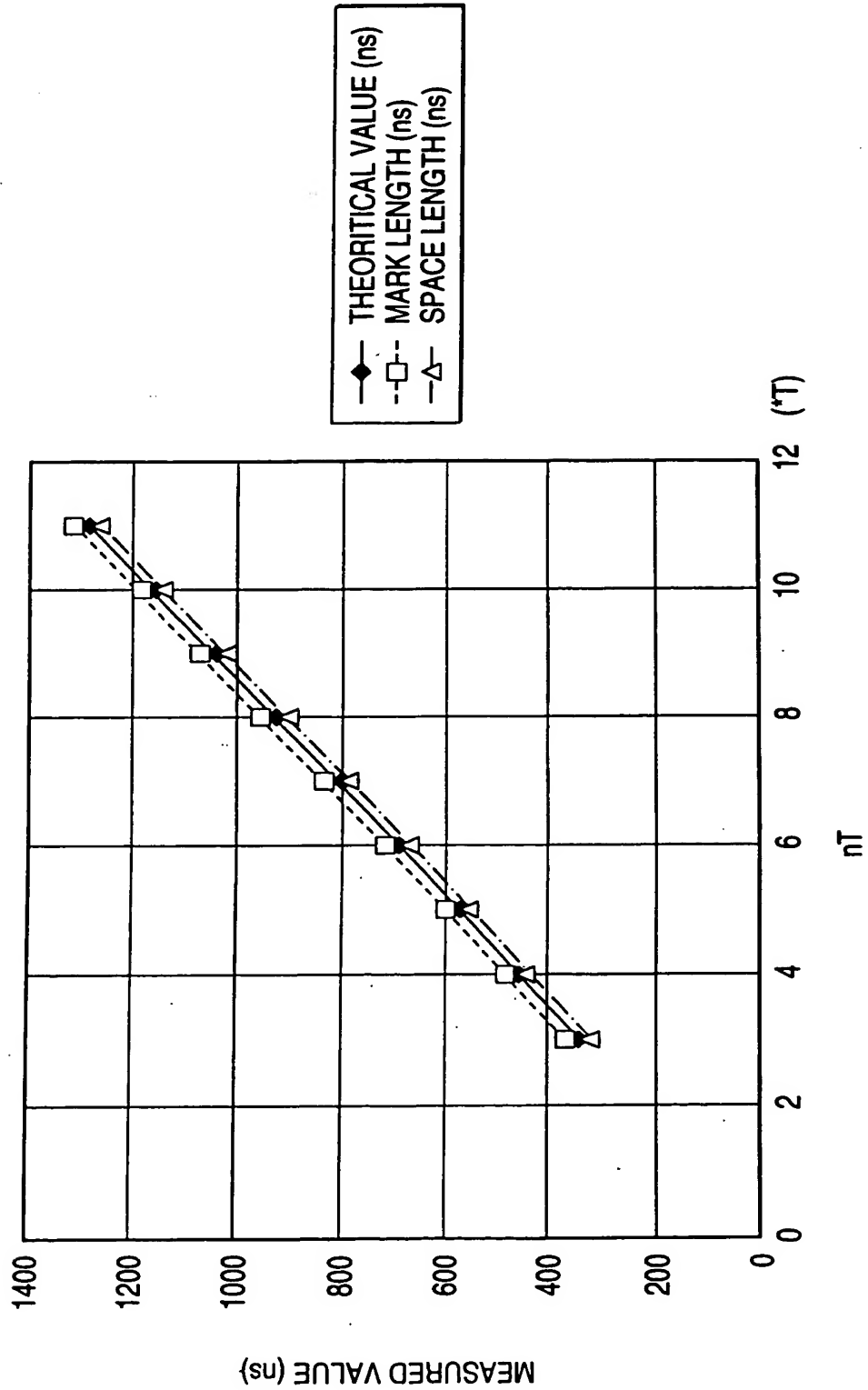
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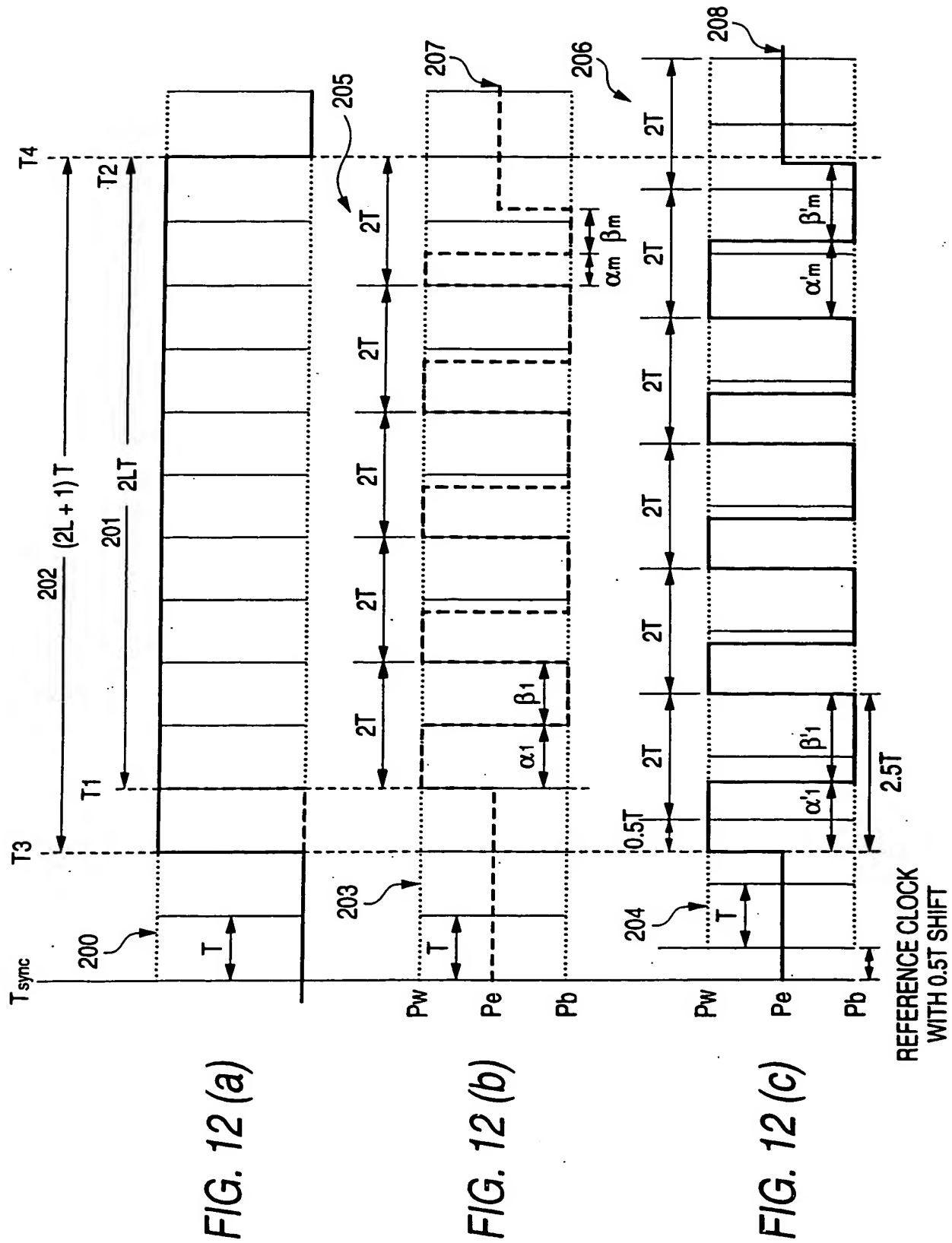
FIG. 9



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FIG. 10





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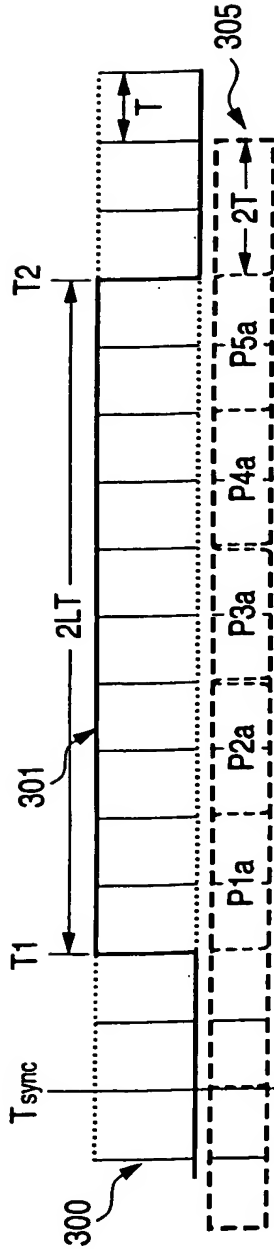


FIG. 13(a)

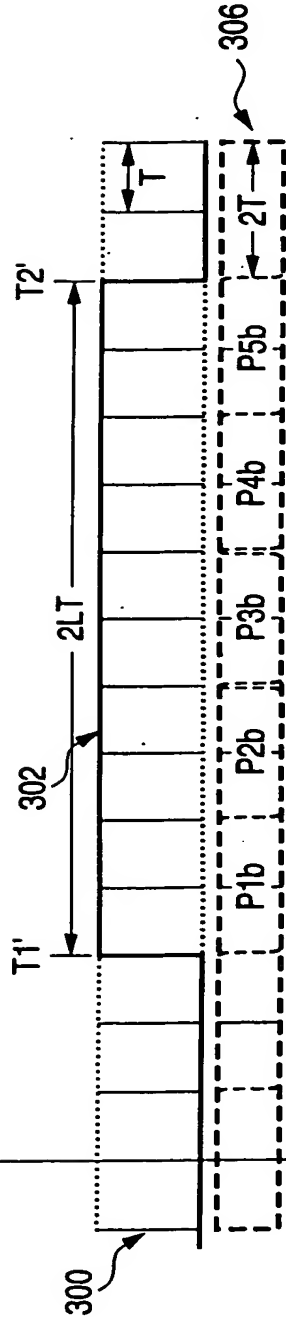


FIG. 13(b)

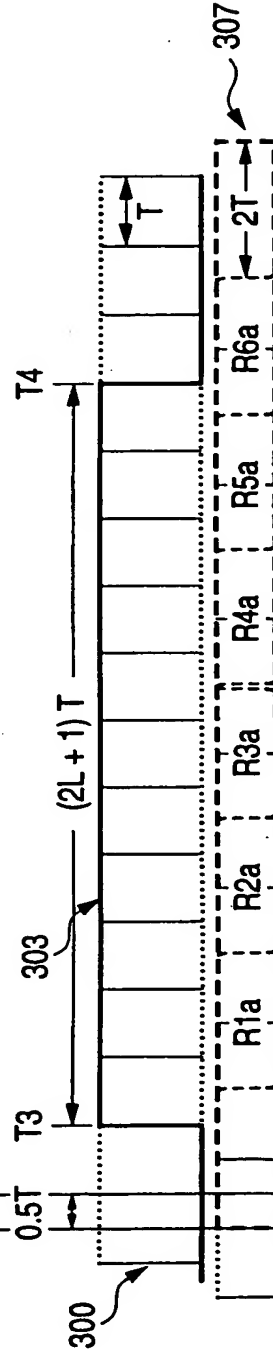


FIG. 13(c)

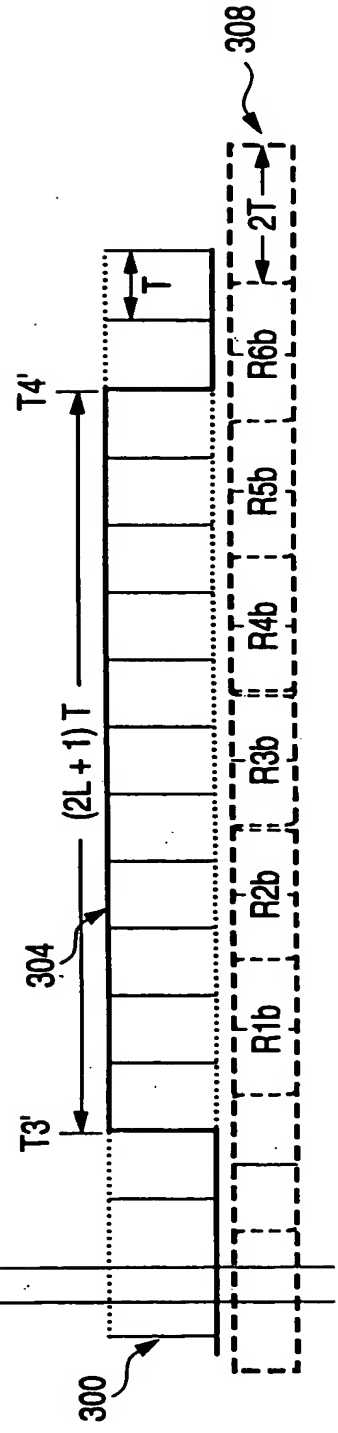


FIG. 13(d)

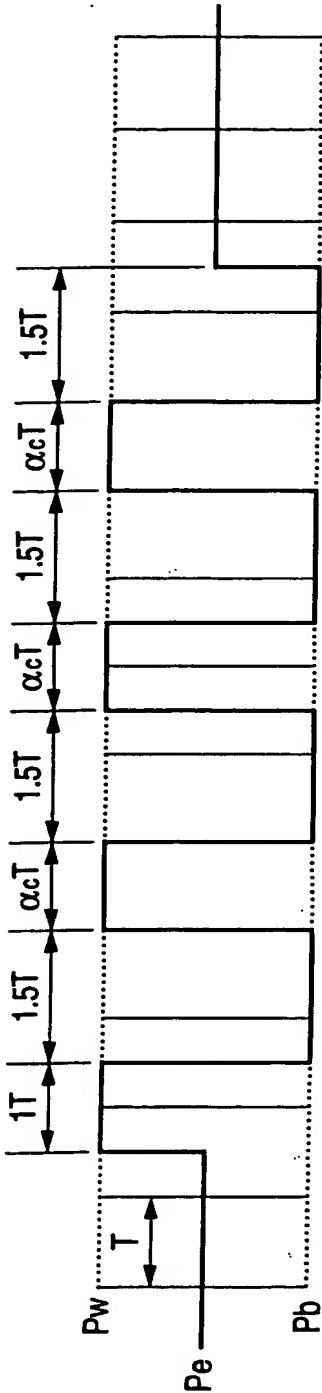


FIG. 14 (a)

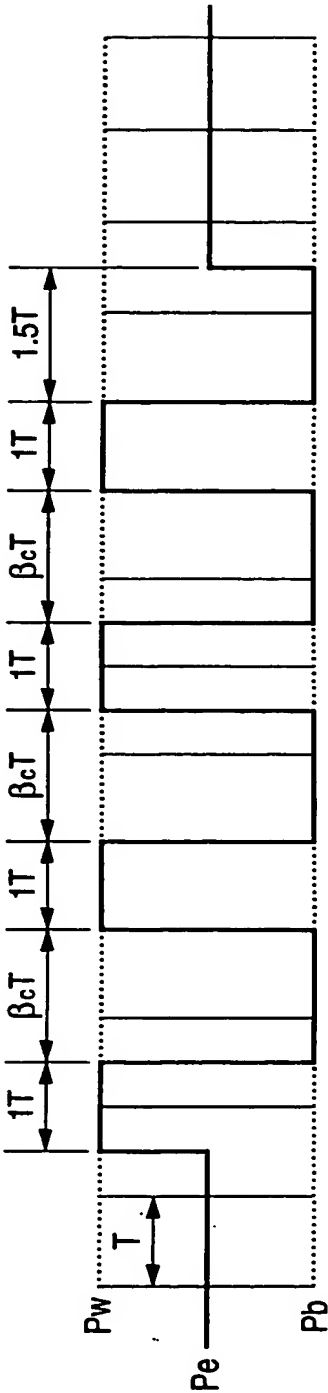


FIG. 14 (b)

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FIG. 15 (a)

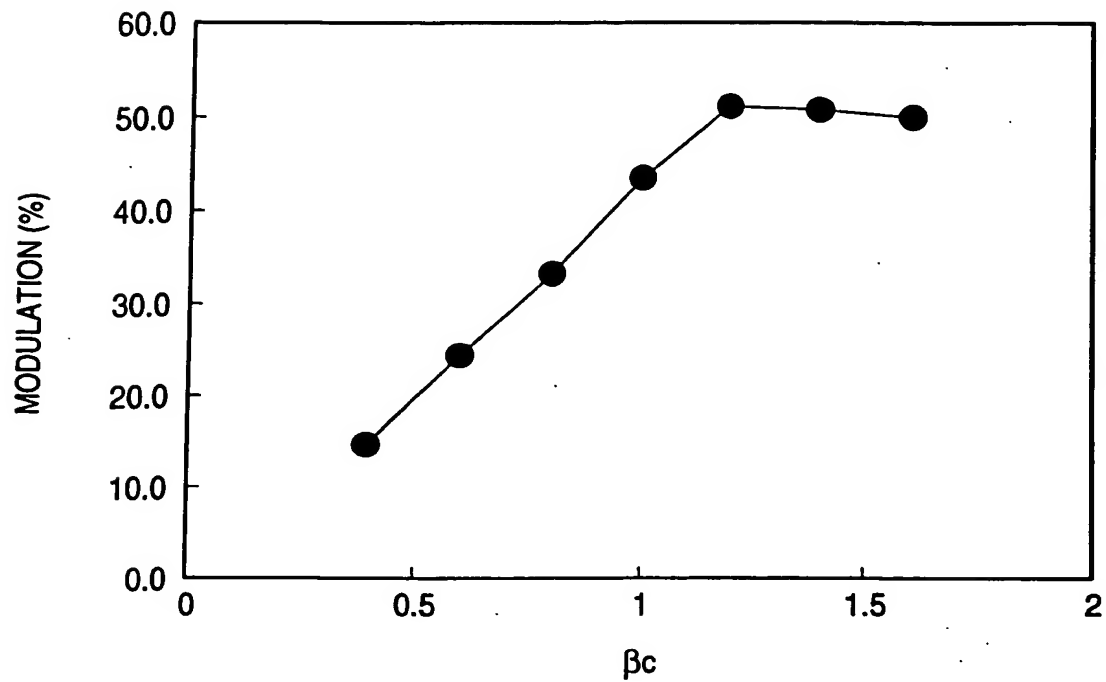
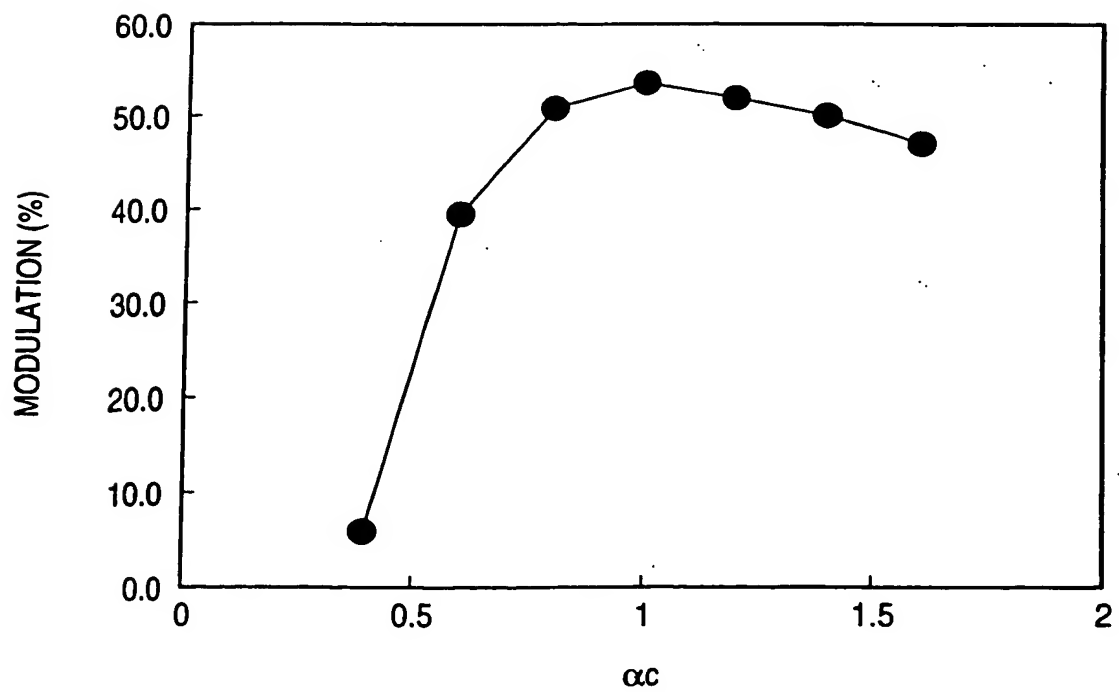


FIG. 15 (b)



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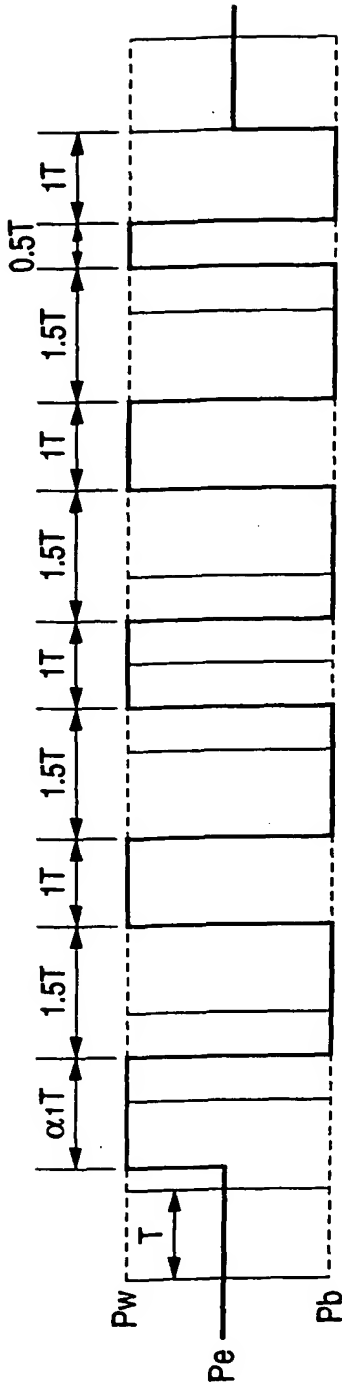


FIG. 16 (a)

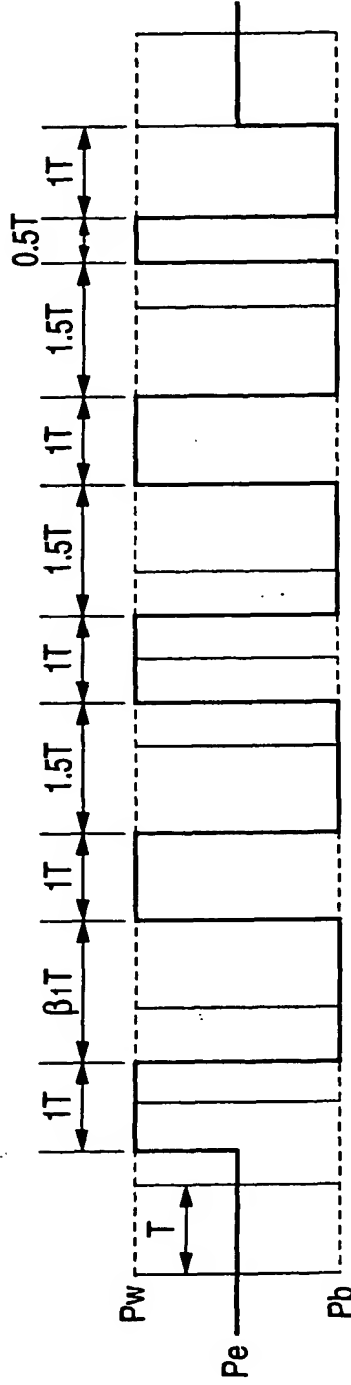


FIG. 16 (b)

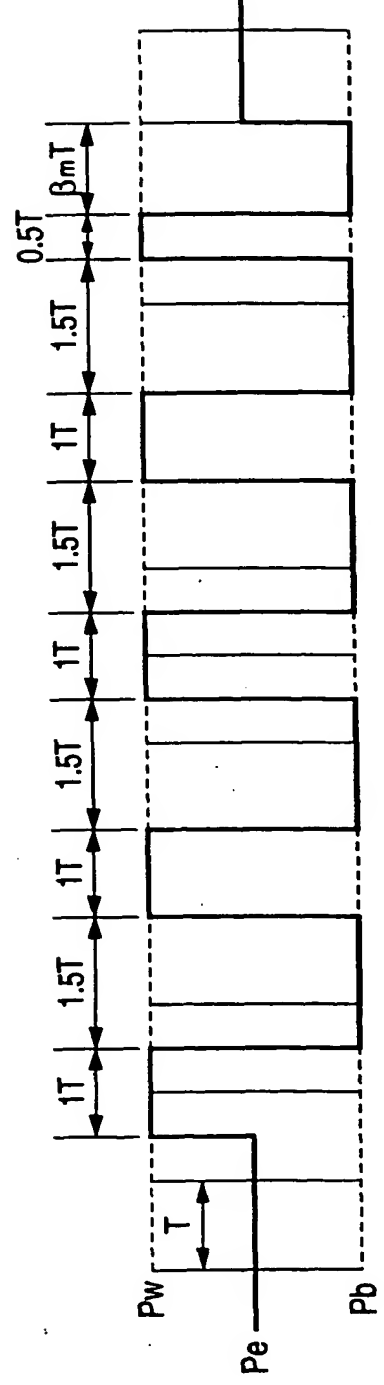


FIG. 16 (c)

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FIG. 17 (a)

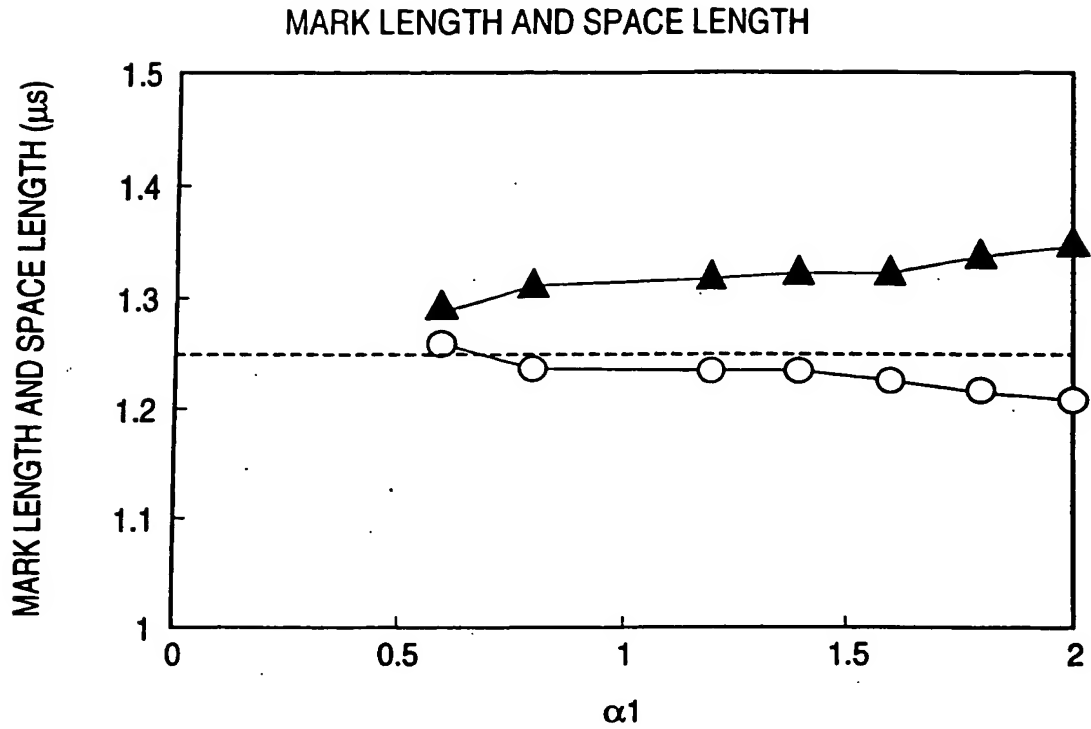
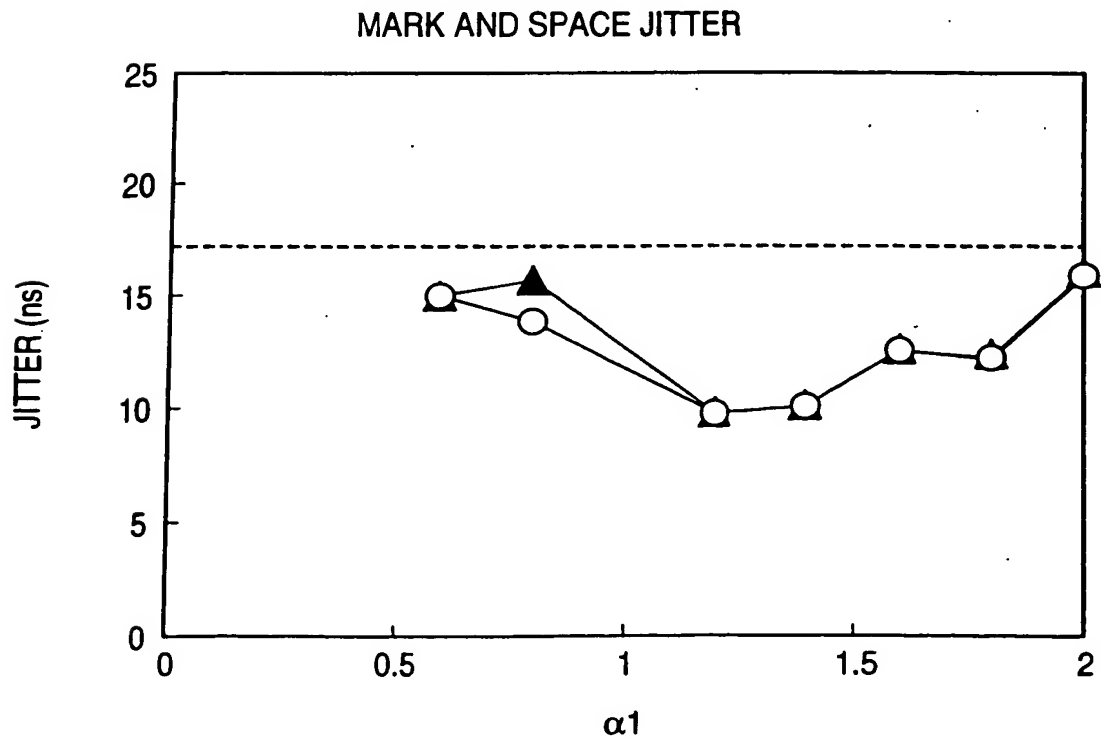


FIG. 17 (b)



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FIG. 18 (a)

MARK LENGTH AND SPACE LENGTH

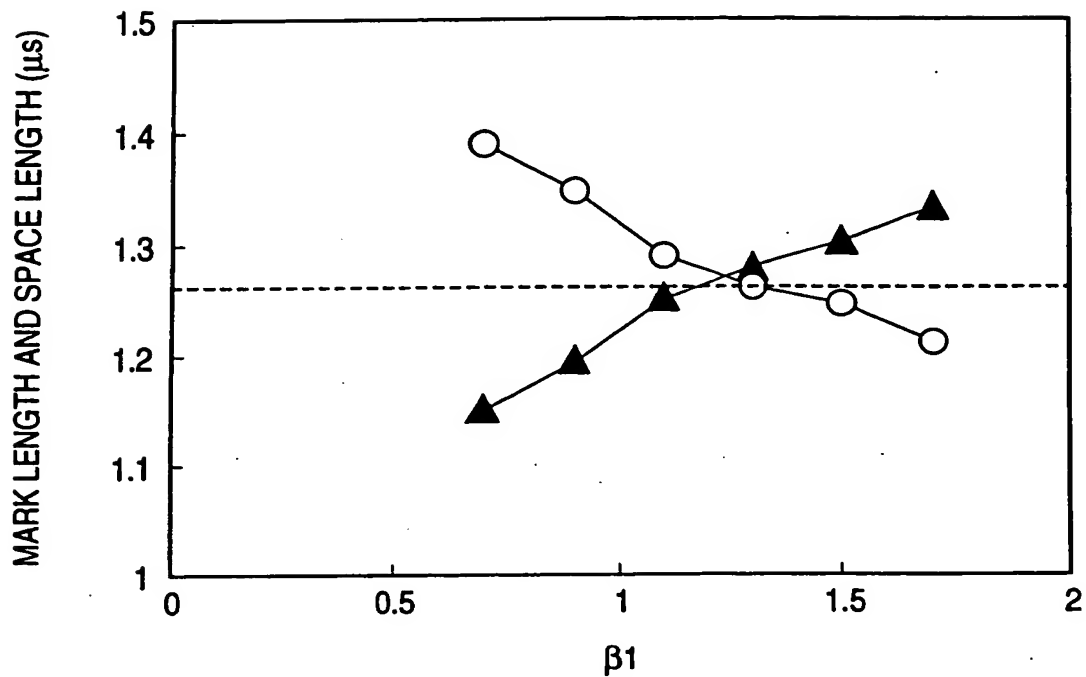
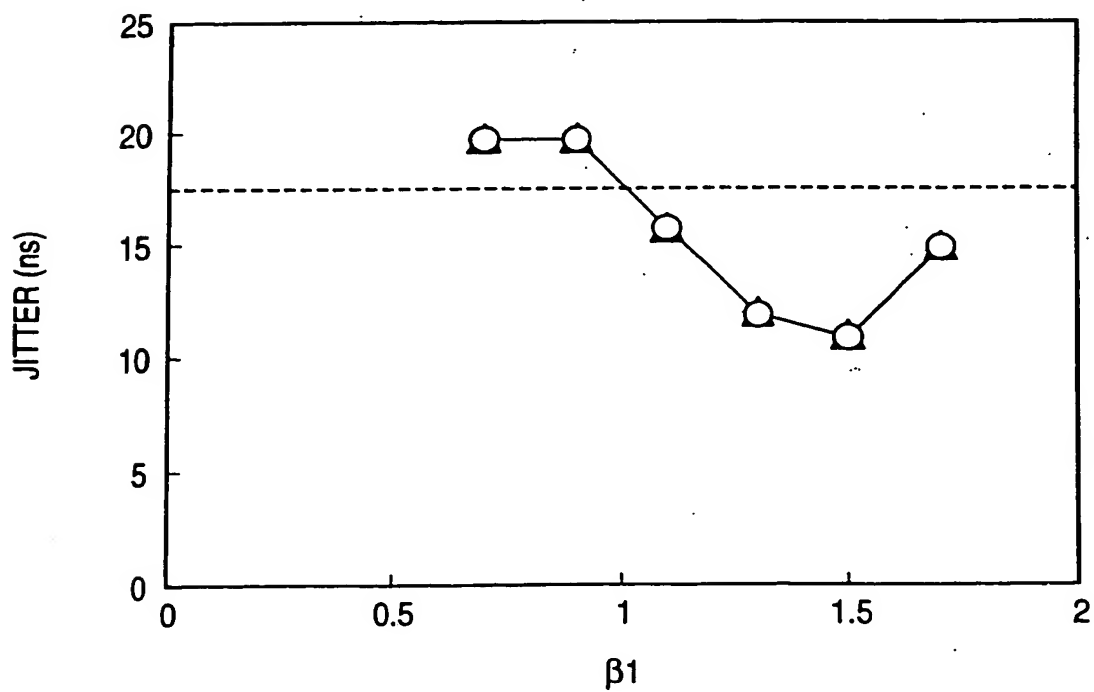


FIG. 18 (b)

MARK AND SPACE JITTER



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FIG. 19 (a)

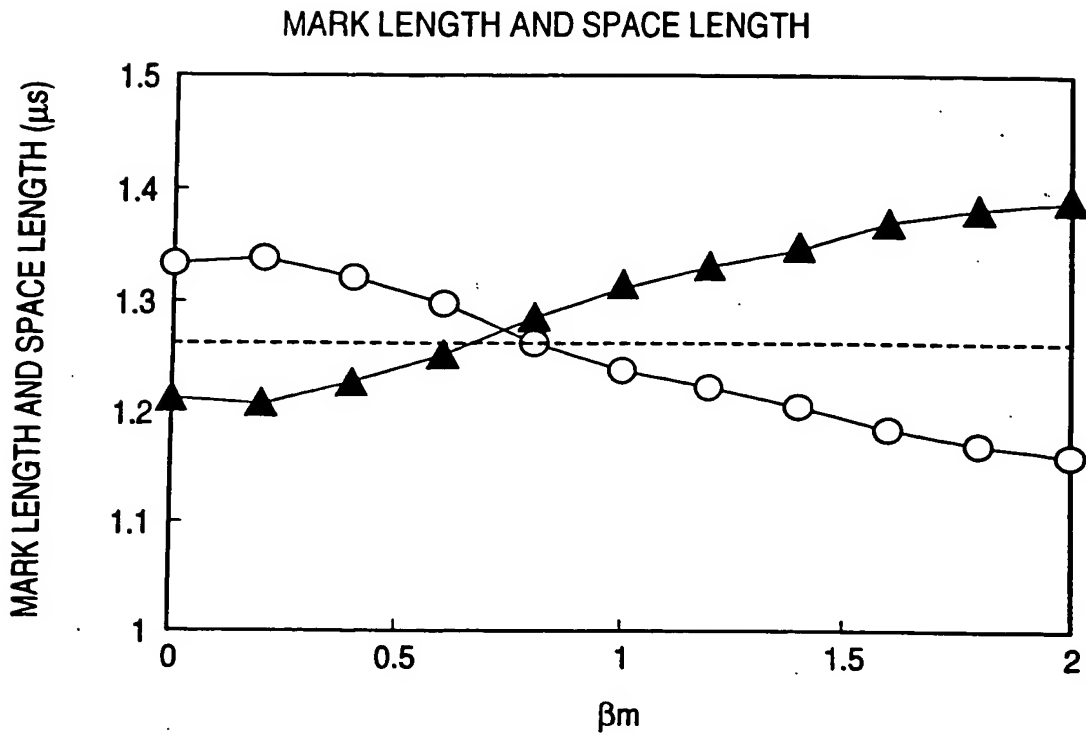
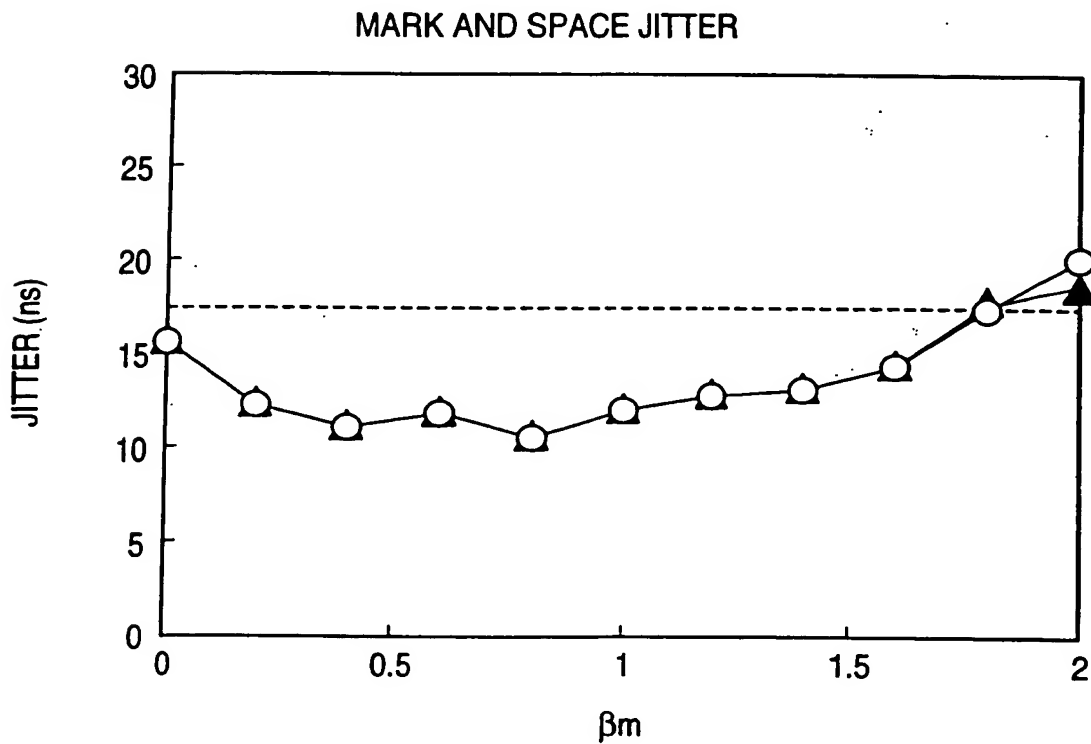


FIG. 19 (b)



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FIG. 20

	$\alpha 1$	$\beta 1$	$\alpha 2$	$\beta 2$	$\alpha 3$	$\beta 3$	$\alpha 4$	$\beta 4$	$\alpha 5$	$\beta 5$
3T	1.2T	1.5T								
4T	0.8T	1.2T	0.7T	0.9T						
5T	1.0T	1.5T	1.0T	1.1T						
6T	0.8T	1.2T	0.8T	1.2T	0.7T	0.9T				
7T	1.0T	1.5T	0.8T	1.2T	1.0T	1.1T				
8T	0.8T	1.2T	0.8T	1.2T	0.8T	1.2T	0.7T	0.9T		
9T	1.0T	1.5T	0.8T	1.2T	0.8T	1.2T	1.0T	1.1T		
10T	0.8T	1.2T	0.8T	1.2T	0.8T	1.2T	0.8T	1.2T	0.7T	0.9T
11T	1.0T	1.5T	0.8T	1.2T	0.8T	1.2T	0.8T	1.2T	1.0T	1.1T

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FIG. 21 (a)

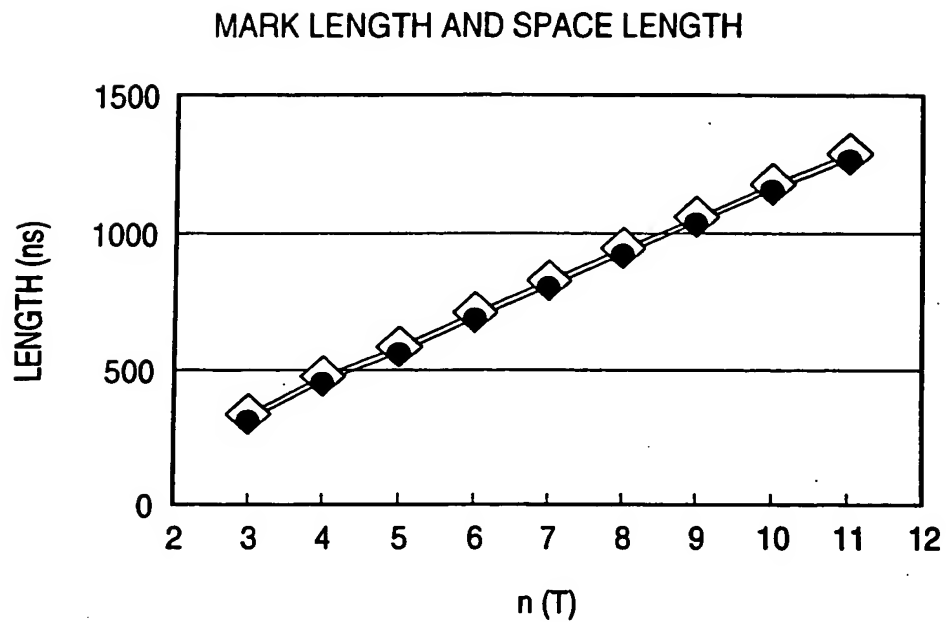
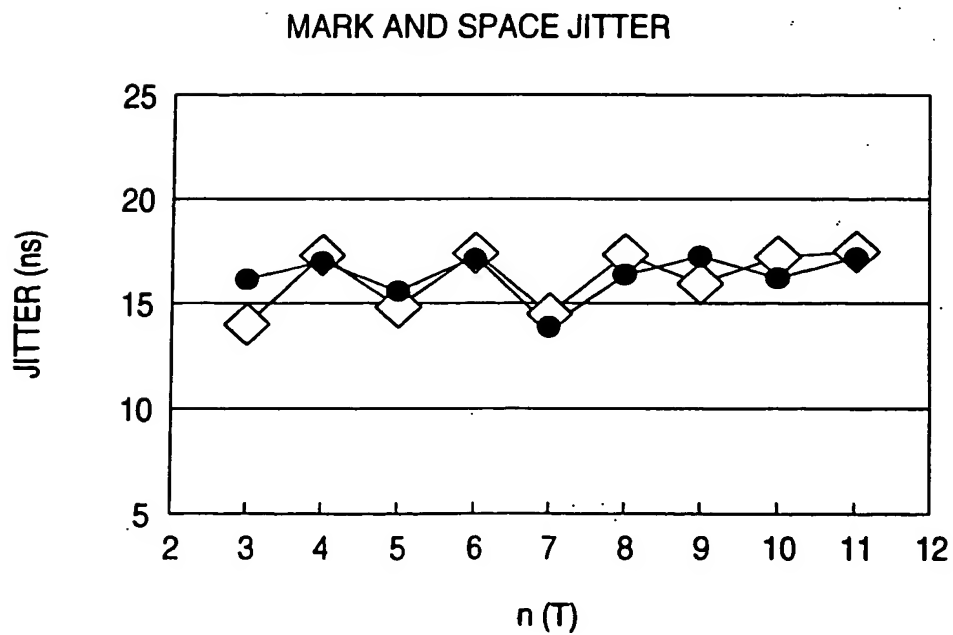


FIG. 21 (b)



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FIG. 22

	$\alpha 1$	$\beta 1$	$\alpha 2$	$\beta 2$	$\alpha 3$	$\beta 3$	$\alpha 4$	$\beta 4$	$\alpha 5$	$\beta 5$
3T	0.8T	2.4T								
4T	0.6T	1.4T	0.5T	1.4T						
5T	0.6T	1.9T	0.6T	1.8T						
6T	0.6T	1.4T	0.5T	1.5T	0.5T	1.4T				
7T	0.6T	1.9T	0.5T	1.5T	0.6T	1.8T				
8T	0.6T	1.4T	0.5T	1.5T	0.5T	1.5T	0.5T	1.4T		
9T	0.6T	1.9T	0.5T	1.5T	0.5T	1.5T	0.6T	1.8T		
10T	0.6T	1.4T	0.5T	1.5T	0.5T	1.5T	0.5T	1.5T	0.5T	1.4T
11T	0.6T	1.9T	0.5T	1.5T	0.5T	1.5T	0.5T	1.5T	0.8T	1.8T

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FIG. 23 (a)

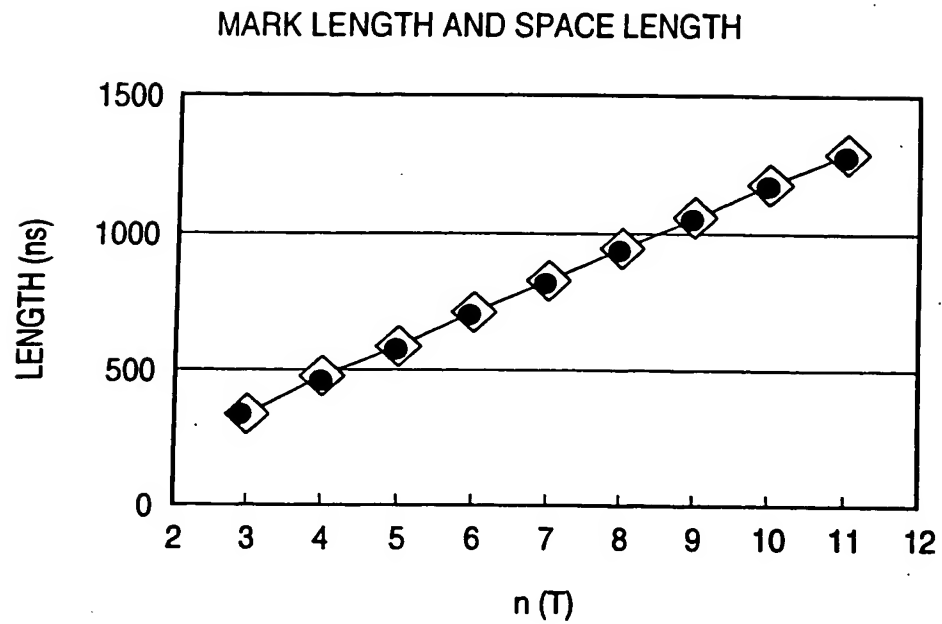
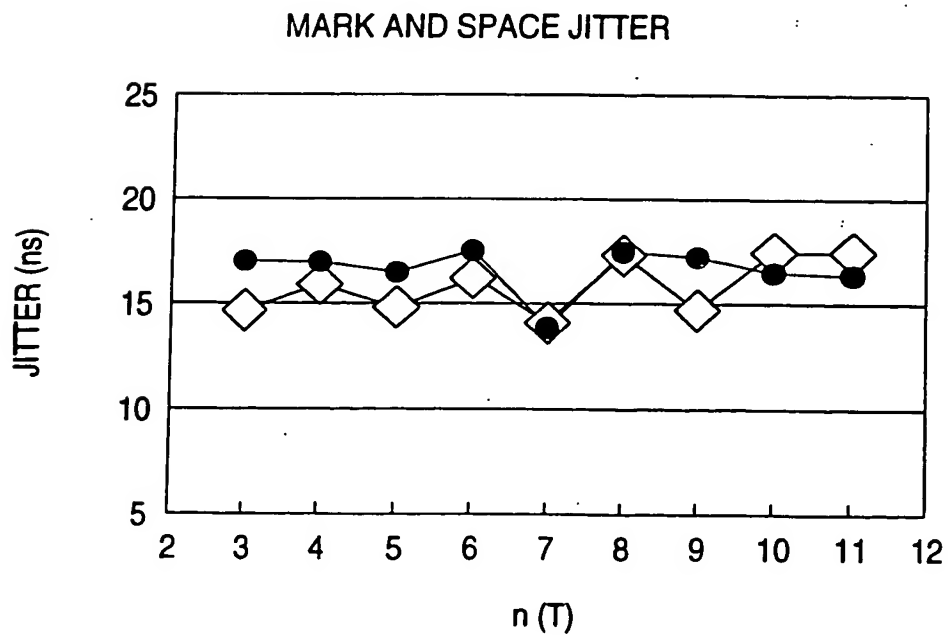
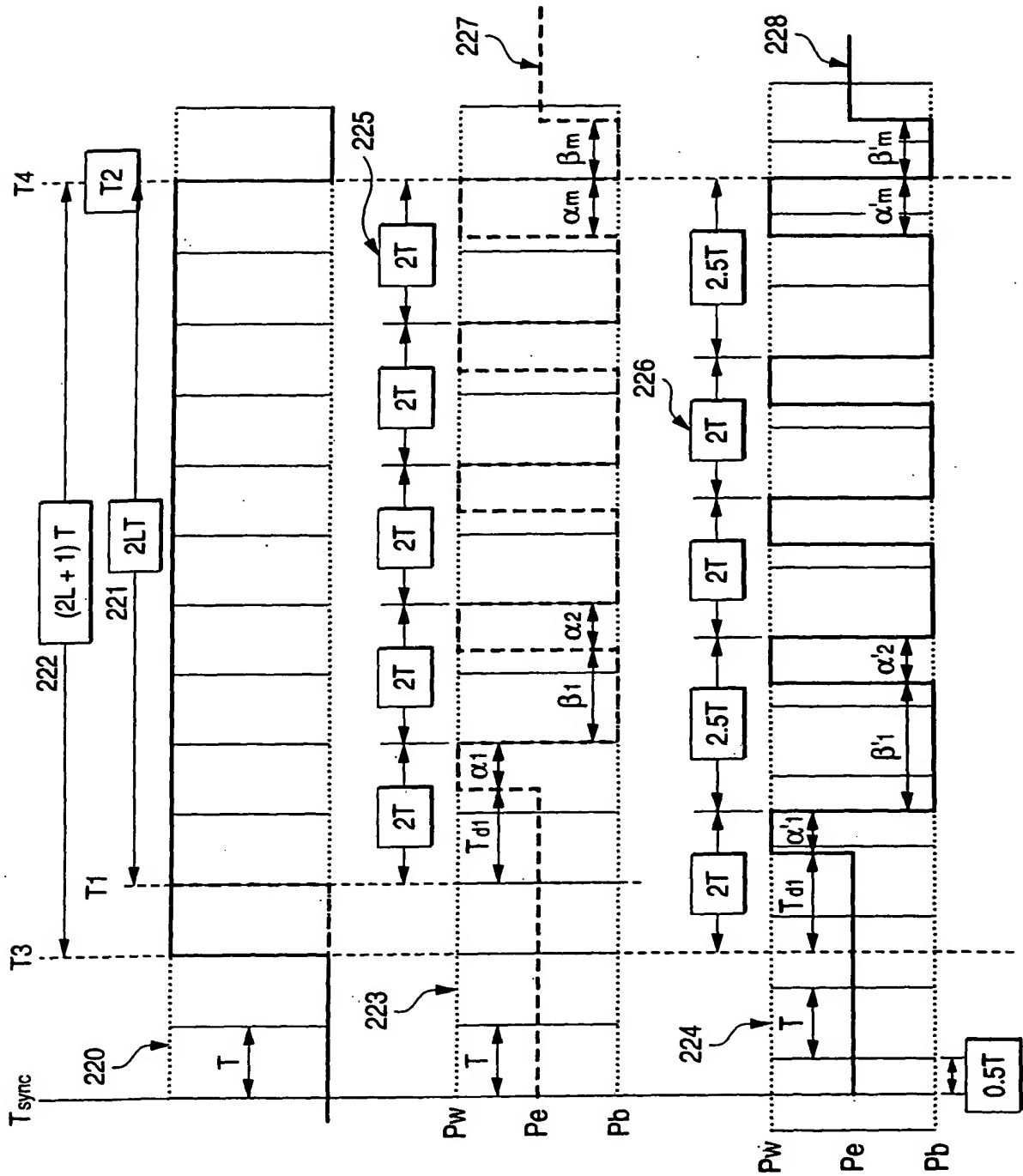


FIG. 23 (b)





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FIG. 25 (a)

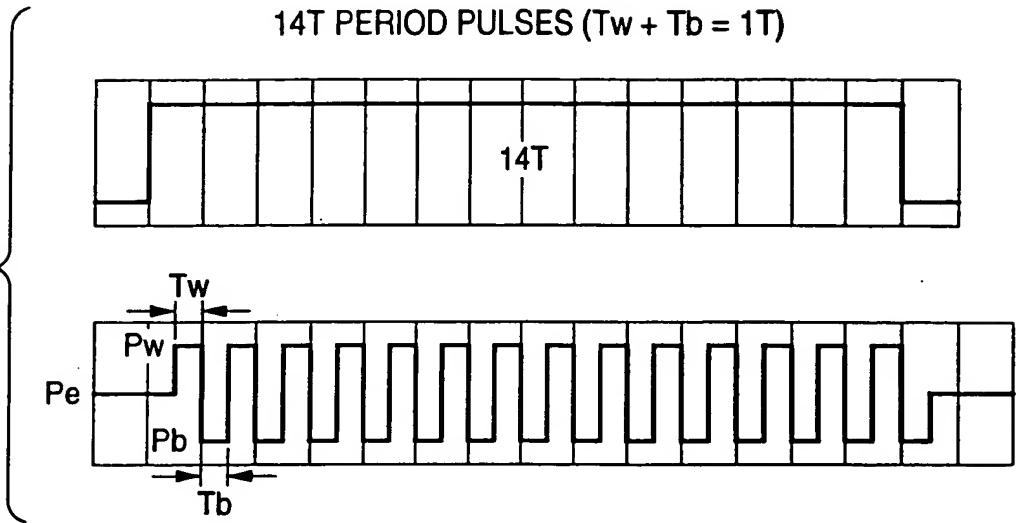


FIG. 25 (b)

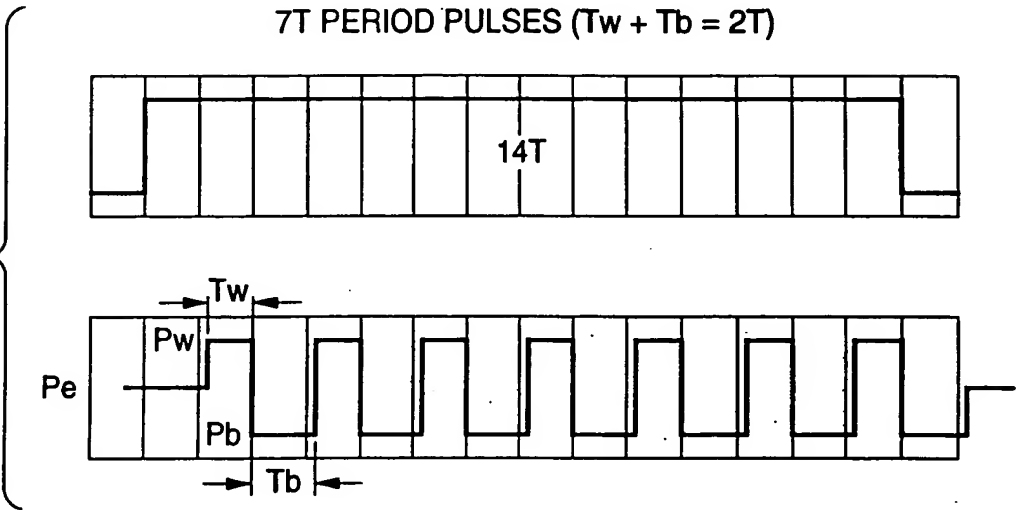
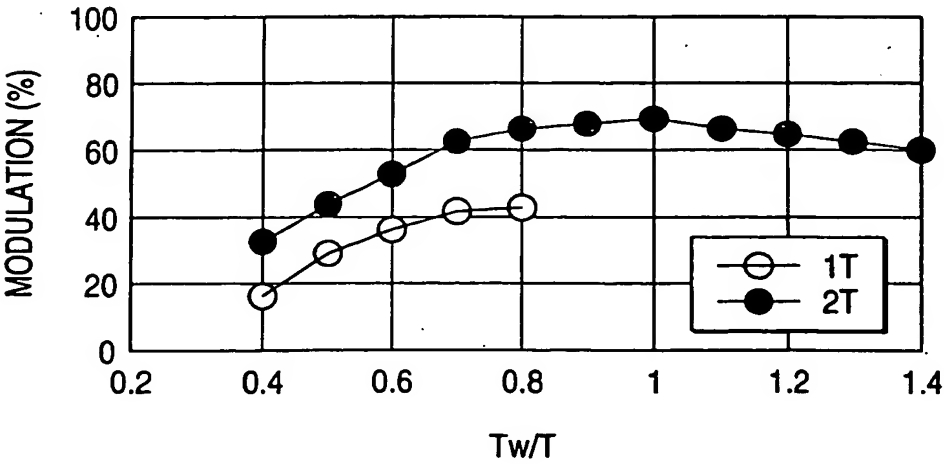


FIG. 25 (c)



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FIG. 27 (a)

RECORDING POWER DEPENDENCY OF MODULATION

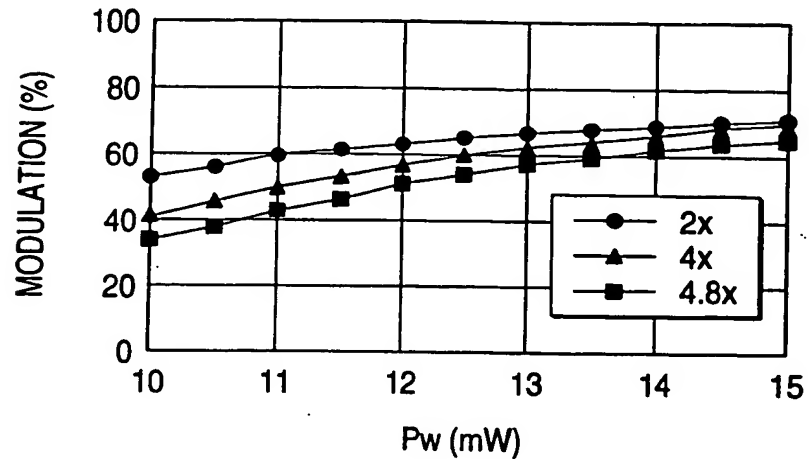


FIG. 27 (b)

RECORDING POWER DEPENDENCY OF EDGE-TO-CLOCK JITTER

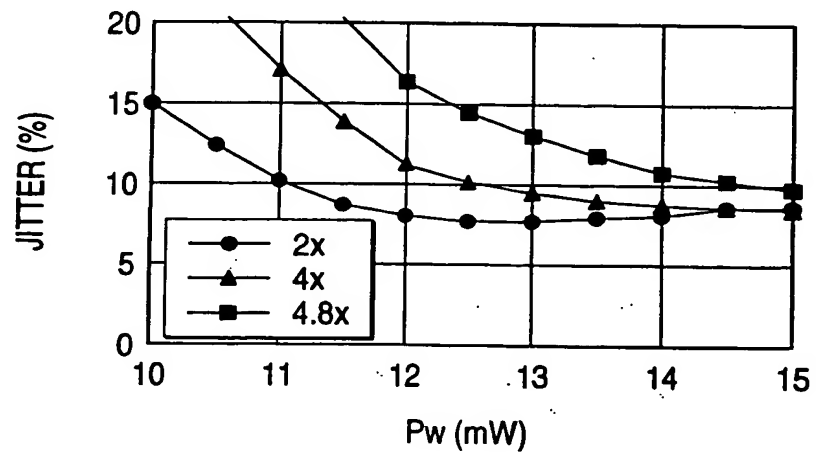
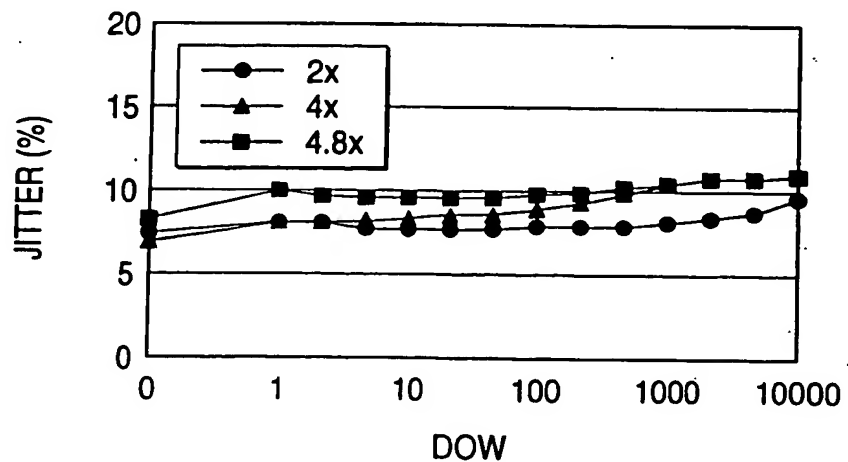


FIG. 27 (c)

OVERWRITE CYCLE DEPENDENCY OF EDGE-TO-CLOCK JITTER



[illegible]